Welcome to the proceedings of the Software Verification and Testing (SVT) track at the 2013 ACM Symposium on Applied Computing (SAC). The SVT track focuses on new results in formal verification and testing, as well as development of technologies to improve the usability of formal methods in software engineering. This track has a tradition in SAC, as it was organized each year since 2006.

The main topics of the special track include, but are not limited to: tools and techniques for verification of large scale software systems; real world applications and case studies applying software verification; static and runtime analysis; abstract interpretation; model checking; theorem proving; refinement and correct by construction development; model-based testing; verification-based testing; run-time verification; symbolic execution and partial evaluation; analysis methods for dependable systems; and software certification and proof carrying code.

This year we received a number of 36 submissions, of which two were withdrawn. All the remaining 34 submissions received at least 3 review reports (most received 4) provided by our international Program Committee (PC) and/or external reviewers. Afterwards, the submissions were extensively discussed in the virtual meeting of the PC and the PC decided to select 8 papers among the submissions for presentation in the symposium and publication in its proceedings, resulting in an acceptance rate of 23%. The accepted papers cover different topics in the area of testing (4 accepted papers), formal verification and static analysis (4 accepted papers).

We would like to thank all the members of the program committee for their hard work during the review process, the external reviewers for providing review reports, the authors for submitting their papers to the track, and the participants for attending it. Also, we would like to thank the program chairs and the organizers of the ACM SAC 2013 who provided the infrastructure for this event.

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